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L8	12	subpicture and signal and YUV and value and alpha	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 21:38
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Inventor Name Search Result

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Last Name = PIAZZA
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Application#	Patent#	Status	Date Filed	Title	Inventor Name
09478999	Not Issued	71	01/07/2000	MULTI-PASS 4:2:0 SUBPICTURE BLENDING	PIAZZA, THOMAS
11023639	Not Issued	41	12/29/2004	Efficient Z testing	PIAZZA, THOMAS
11024909	Not Issued	30	12/30/2004	Dataport and methods thereof	PIAZZA, THOMAS
60080270	Not Issued	159	04/01/1998	LINEAR SURFACE MEMORY TO SPATIAL TILING ALGORITHM/MECHANISM	PIAZZA, THOMAS
09470099	6393579	150	12/21/1999	METHOD AND APPARATUS FOR SAVING POWER AND IMPROVING PERFORMANCE IN A COLLAPSABLE PIPELINE USING GATED CLOCKS	PIAZZA, THOMAS A.
09476658	6760031	150	12/31/1999	UPGRADING AN INTEGRATED GRAPHICS SUBSYSTEM	PIAZZA, THOMAS A.
09480156	6466226	150	01/10/2000	METHOD AND APPARATUS FOR PIXEL FILTERING USING SHARED FILTER RESOURCE BETWEEN OVERLAY AND TEXTURE MAPPING ENGINES	PIAZZA, THOMAS A.
09502994	7050063	150	02/11/2000	3-D RENDERING TEXTURE CACHING SCHEME	PIAZZA, THOMAS A.
09735037	6639598	150	12/12/2000	METHOD AND APPARATUS FOR EFFECTIVE LEVEL OF DETAIL SELECTION	PIAZZA, THOMAS A.
09823126	Not Issued	163	03/30/2001	Prefetch canceling based on most recent accesses	PIAZZA, THOMAS A.
10033439	6999091	150	12/28/2001	DUAL MEMORY CHANNEL INTERLEAVING FOR GRAPHICS AND VIDEO	PIAZZA, THOMAS A.
10033440	6792516	150	12/28/2001	MEMORY ARBITER WITH INTELLIGENT PAGE GATHERING LOGIC	PIAZZA, THOMAS A.
10038941	7035984	150	12/31/2001	MEMORY ARBITER WITH GRACE AND CEILING PERIODS AND INTELLIGENT PAGE GATHERING LOGIC	PIAZZA, THOMAS A.
10039791	6762765	150	12/31/2001	BANDWIDTH REDUCTION FOR ZONE	PIAZZA, THOMAS A.

				RENDERING VIA SPLIT VERTEX BUFFERS	
10135149	7139890	150	04/30/2002	METHODS AND ARRANGEMENTS TO INTERFACE MEMORY	PIAZZA, THOMAS A.
10233581	7158147	150	09/04/2002	METHOD AND APPARATUS FOR PIXEL FILTERING USING SHARED FILTER RESOURCE BETWEEN OVERLAY AND TEXTURE MAPPING ENGINES	PIAZZA, THOMAS A.
10329202	7268779	150	12/24/2002	Z-BUFFERING TECHNIQUES FOR GRAPHICS RENDERING	PIAZZA, THOMAS A.
10335377	Not Issued	41	12/30/2002	Run length encoded digital image	PIAZZA, THOMAS A.
10335423	7212676	150	12/30/2002	MATCH MSB DIGITAL IMAGE COMPRESSION	PIAZZA, THOMAS A.
10750090	Not Issued	30	12/31/2003	Behavioral model based multi-threaded architecture	PIAZZA, THOMAS A.
10750583	Not Issued	41	12/31/2003	Processing architecture having passive threads and active semaphores	PIAZZA, THOMAS A.
10750589	Not Issued	41	12/31/2003	Visual and graphical data processing using a multi-threaded architecture	PIAZZA, THOMAS A.
10816052	Not Issued	71	03/31/2004	Render-cache controller for multithreading, multi-core graphics processor	PIAZZA, THOMAS A.
10831506	Not Issued	161	04/23/2004	Upgrading an integrated graphics subsystem	PIAZZA, THOMAS A.
10856054	6950108	150	05/27/2004	BANDWIDTH REDUCTION FOR RENDERING USING VERTEX DATA	PIAZZA, THOMAS A.
10879460	Not Issued	41	06/29/2004	Conditional stack for a single instruction, multiple data execution engine	PIAZZA, THOMAS A.
10932395	7051172	150	09/01/2004	MEMORY ARBITER WITH INTELLIGENT PAGE GATHERING LOGIC	PIAZZA, THOMAS A.
11012688	Not Issued	71	12/15/2004	Hardware stack having entries with a data portion and associated counter	PIAZZA, THOMAS A.
11025105	Not Issued	71	12/29/2004	Determining a register file region based at least in part on a value in an index register	PIAZZA, THOMAS A.
11224643	Not Issued	30	09/12/2005	Dynamic allocation of a buffer across multiple clients in a threaded processor	PIAZZA, THOMAS A.
11344030	Not Issued	41	01/30/2006	3-D rendering texture caching scheme	PIAZZA, THOMAS A.
11619124	Not Issued	41	01/02/2007	PIXEL FILTERING USING SHARED FILTER RESOURCE BETWEEN OVERLAY AND TEXTURE MAPPING ENGINES	PIAZZA, THOMAS A.
11647608	Not Issued	30	12/30/2006	Thread queuing method and apparatus	PIAZZA, THOMAS A.
11648260	Not Issued	30	12/29/2006	Methods and apparatuses for compaction and/or decompaction	PIAZZA, THOMAS A.

11712046	Not Issued	30	02/27/2007	Match MSB digital image compression	PIAZZA, THOMAS A.
11770120	Not Issued	25	06/28/2007	CACHE FOR A MULTI THREAD AND MULTI CORE SYSTEM AND METHODS THEREOF	PIAZZA, THOMAS A.
60638427	Not Issued	159	12/23/2004	Dynamic allocation of a buffer across multiple clients in a threaded processor	PIAZZA, THOMAS A.
07116708	4958305	150	11/04/1987	POLYGON EDGE CLIPPING	PIAZZA, THOMAS A.
07377687	Not Issued	161	07/10/1989	SPATIAL AUGMENTATION OF VERTICES FOR LEVEL OF DETAIL TRANSITION	PIAZZA, THOMAS A.
07695439	Not Issued	166	05/03/1991	SPATIAL AUGMENTATION OF VERTICES AND CONTINUOUS LEVEL OF DETAIL TRANSITION FOR SMOOTHLY VARYING TERRAIN POLYGON DENSITY	PIAZZA, THOMAS A.
08115727	5367615	150	09/02/1993	SPATIAL AUGMENTATION OF VERTICES AND CONTINUOUS LEVEL OF DETAIL TRANSITION FOR SMOOTHLY VARYING TERRAIN POLYGON DENSITY	PIAZZA, THOMAS A.
09053589	6072505	150	04/01/1998	METHOD AND APPARATUS TO EFFICIENTLY INTERPOLATE POLYGON ATTRIBUTES IN TWO DIMENSIONS AT A PRESCRIBED CLOCK RATE	PIAZZA, THOMAS A.
09053590	6091428	150	04/01/1998	FRAME BUFFER MEMORY SYSTEM FOR REDUCING PAGE MISSES WHEN RENDERING WITH COLOR AND Z BUFFERS	PIAZZA, THOMAS A.
09053591	6191793	150	04/01/1998	METHOD AND APPARATUS FOR TEXTURE LEVEL OF DETAIL DITHERING	PIAZZA, THOMAS A.
09061383	6204857	150	04/16/1998	METHOD AND APPARATUS FOR EFFECTIVE LEVEL OF DETAIL SELECTION	PIAZZA, THOMAS A.
09226965	6330646	150	01/08/1999	An arbitration mechanism for a computer system having a unified memory architecture	PIAZZA, THOMAS A.
09227174	Not Issued	124	01/07/1999	METHOD AND APPARATUS FOR PERFORMING MOTION COMPENSATION IN A TEXTURE MAPPING ENGINE	PIAZZA, THOMAS A.
09234133	6433790	150	01/19/1999	METHODS AND SYSTEMS FOR RENDERING LINE AND POINT FEATURES FOR DISPLAY	PIAZZA, THOMAS A.
09282361	Not Issued	161	03/31/1999	LINEAR SURFACE MEMORY TO SPATIAL TILING ALGORITHM/MECHANISM	PIAZZA, THOMAS A.

· 09470101	6710784	150	12/21/1999	METHOD AND APPARATUS FOR PERFORMING A VERTICAL SCALE FILTER FUNCTION IN A GRAPHICS DEVICE USING A SINGLE LINE BUFFER	PIAZZA, THOMAS A.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
60119681	Not Issued	159	02/11/1999	3-D RENDERING TEXTURE CACHING SCHEME	PIAZZA, THOMAS A.
60308994	Not Issued	159	07/30/2001	Broadband ultrasonic transducer with multiple overtones	PIAZZA, TIMOTHY
06933537	Not Issued	161	02/06/1987	BRUSHWACKERS	PIAZZA, TIMOTHY L.
10208433	7019439	150	07/30/2002	HIGH POWER ULTRASONIC TRANSDUCER WITH BROADBAND FREQUENCY CHARACTERISTICS AT ALL OVERTONES AND HARMONICS	PIAZZA, TIMOTHY W.
09480622	6816167	150	01/10/2000	ANISOTROPIC FILTERING TECHNIQUE	PIAZZA, TOM
09480874	6633299	150	01/10/2000	METHOD AND APPARATUS FOR IMPLEMENTING SMART ALLOCATION POLICIES FOR A SMALL FRAME BUFFER CACHE SERVING 3D AND 2D STREAMS	PIAZZA, TOM

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